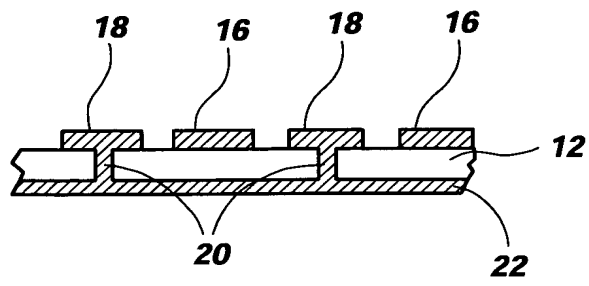
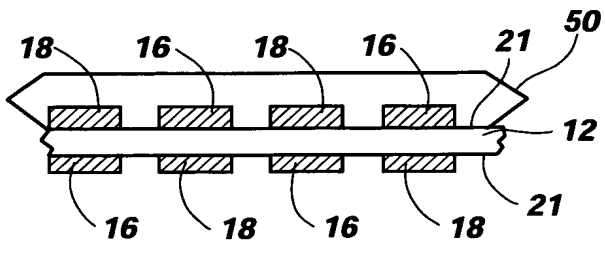




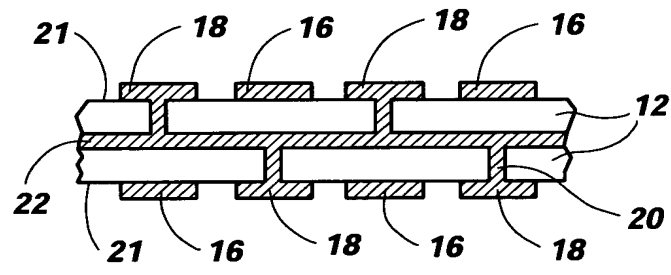
TITLE: METHOD AND APPARATUS OF INTERPOSING  
VOLTAGE REFERENCE TRACES BETWEEN SIGNAL  
TRACES IN SEMICONDUCTOR DEVICES  
Serial No.: 09/548,942  
Docket No.: 2269-4161US  
**REPLACEMENT SHEET**



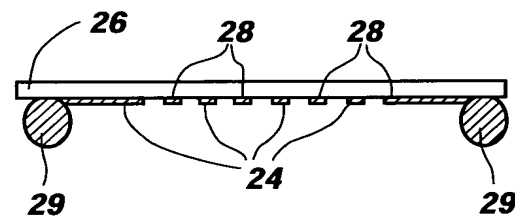
**Fig. 6**



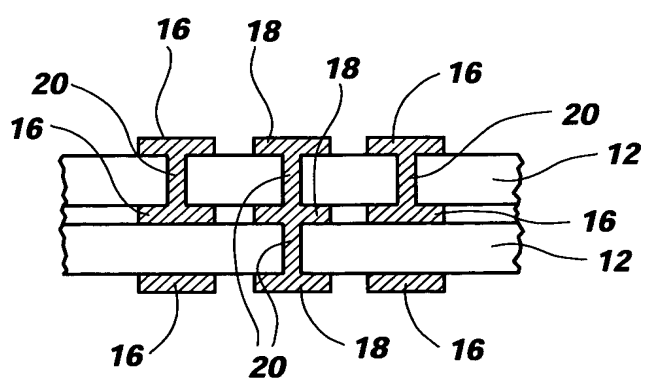
**Fig. 8**



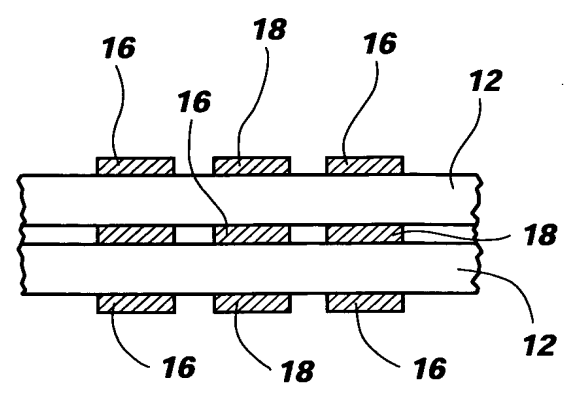
**Fig. 7**



**Fig. 11**



**Fig. 10**



**Fig. 9**